

Profi-Load: An FPGA-Based Solution for Generating Network Load in Profinet Communication

Ahmad Khaliq, Sangeet Saha, Bina Bhatt, Dongbing Gu and Klaus McDonald-Maier

Abstract—Industrial automation has received a considerable attention in the last few years with the rise of Internet of Things (IoT). Specifically, industrial communication network technology such as Profinet has proved to be a major game changer for such automation. However, industrial automation devices often have to exhibit robustness to dynamically changing network conditions and thus, demand a rigorous testing environment to avoid any safety-critical failures. Hence, in this paper, we have proposed an FPGA-based novel framework called “Profi-Load” to generate Profinet traffic with specific intensities for a specified duration of time. The proposed *Profi-Load* intends to facilitate the performance testing of the industrial automated devices under various network conditions. By using the advantage of inherent hardware parallelism and re-configurable features of FPGA, *Profi-Load* is able to generate Profinet traffic efficiently. Moreover, it can be reconfigured on the fly as per the specific requirements. We have developed our proposed *Profi-Load* framework by employing the Xilinx-based “NetJury” device which belongs to Zynq-7000 FPGA family. A series of experiments have been conducted to evaluate the effectiveness of *Profi-Load* and it has been observed that *Profi-Load* is able to generate precise load at a constant rate for stringent timing requirements. Furthermore, a suitable Human Machine Interface (HMI) has also been developed for quick access to our framework. The HMI at the client side can directly communicate with the NetJury device and parameters such as, required load amount, number of packet(s) to be sent or desired time duration can be selected using the HMI.

I. INTRODUCTION

A significant increase in the demand for Ethernet based infrastructure has been observed in the area ranging from automation technology to transport infrastructure [1]. In comparison to traditional office Ethernet based networks, the modern Ethernet technology usage is much more challenging. Recent trends of technology exhibit the requirement of deterministic behavior of network. Amidst such demand, Profinet [2] is found to be an established real-time Ethernet based standard that is optimized for communication in automated industrial environments.

Industrial automation systems are complex in nature and usually structured into several hierarchical levels. Each of the level demands appropriate communication. These hierarchical levels can be categorized as 1) field-level networks, 2) control-level networks and 3) information-level networks [3]. At the automation field level, various sensors, actuators, I/O

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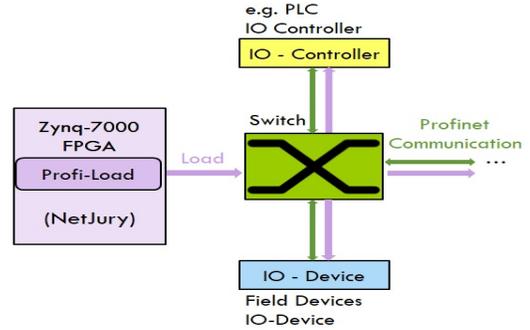


Fig. 1. *Profi-Load* in Nutshell

modules and drive units communicate via Profinet. Office- or information-level deals with centralized PCs for monitoring purposes. Automation control level includes controllers like PLC [4] that communicate with actuators and other IO devices with industrial field buses Profibus as illustrated in Figure 2.

For instance, position control of electric AC motors is a widely adopted application in industrial automation environment. In this specific case, the control system requires real-time response within a strict duration. Hence, analysis of some significant network performance is required to evaluate the accomplishments of this requirement. To evaluate the effectiveness of such applications, we need a rigorous testing environment, where we can observe the performance of the application under various network conditions. Real-time characteristics and determinism issues, are the two uttermost important factors in Profinet based communication. To evaluate these two factors, one requires a framework which can generate specific network packets with stringent timing specification (commonly known as *net load* or *Load generation*) [5].

Recently, with the advent of new hardware based solutions, FPGAs have emerged as a bright prospect for computation sensitive applications [6]. Due to its inherent parallelism application, execution speed on this platform is much faster than software-based execution on commercial-of-the-shelf (COTS) hardware. Reconfigurability is another interesting feature that FPGAs exhibit. Due to this unique feature, the same system can be reconfigured multiple times by loading new configuration information (bitstream) given different requirements.

In this paper, we have proposed an FPGA-based solution for load generation in the Profinet network. We coined our current traffic generation framework as “*Profi-Load*” and is shown in Figure 1. In order to make the framework

efficient, we have chosen the FPGA-based Xilinx NetJury device [7], as our implementation platform. The experimental validation of our proposed framework on the aforementioned device showed that *Profi-Load* can effectively achieve required amount of load generation capacity with strict timing restrictions. The main contributions of this article can be stated as:

- Defined a framework, “*Profi-Load*” for generating specific amount of network load in real-time Profinet communication.
- Implemented the framework on an FPGA-based NetJury platform.
- The HMI based user interface on NetJury provides a friendly environment to the user such that specific timing and load generating parameters can be provided.
- *Profi-Load* systematically exploits the reconfigurable feature of FPGA and thus, is capable to configure the NetJury device to assimilate user defined requirements.
- The experimental validation and more specifically, Matlab, Hilscher netANALYZER and tektronix DPO4054B oscilloscope based analysis reveals the efficacy of our proposed framework on the NetJury device.

The remainder of the paper is organized as follows. Section II, gives a brief background about the load generation strategy for industrial networks and related work. The *Profi-Load* framework is discussed in Section III by illustrating different user-defined cases. Section IV, provides a brief discussion of the NetJury device and its associated functionality. Section V, discusses on experimental outcome of the proposed framework with a discussion on the same. Finally, the paper concludes with Section VI.

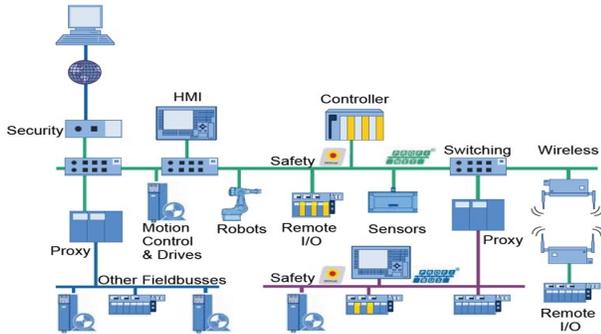


Fig. 2. Profinet network in an automation system [8].

II. BACKGROUND & RELATED WORK

A. Industrial Network Load Generator

Packet generator is a system which allows sending custom packets into the network with desired parameters specified by the user. Similarly, a network load generator can also be considered as a special kind of packet generator which considers a specific rate to send the packet(s) into the network in order to achieve the user-defined % of load. In general, packet generators can be broadly categorized into two types, based on the platforms on which it can be implemented. One type of generator is software based. There are many open

source software-based packet generators including Pktgen [9], scapy [10], ostinato [11] and all of these can run either on Linux or Windows given console or GUI. Another type is the *hardware-based packet generator*, this hardware-based solution involves processing elements like FPGAs, ASIC etc.

The basic hurdles for software-based solutions are its restriction on full line-rate testing. For example, proprietary products such as Ixia [12] are expensive and are not directly available to the open-source community. As an alternative, FPGA-based NetJury device allows an open-source implementation of packet generator coupled with the packet receiver operated at Gigabit/Fast Ethernet line rates. However, such hardware or software based solutions while generating packets, do not consider the amount of load to be maintained in the network, therefore, our proposed *Profi-Load* targets to generate traffic for the desired load as specified by the user.

B. Related Work

In recent past, the research community has focused on developing and improving various aspects of industrial automation systems and Profinet played a pivotal role here [13]. Researches have spun off in various directions to incorporate various advance modification on Profinet performance. In [14], authors depicted the performance analysis of Profinet communication applied in a motion control application. In [15], an actual case study regarding a real Profinet IO network for factory automation has been discussed. The outcomes from their experiments showed that Profinet is able to maintain strict timeliness even when the device is heavily loaded.

A communication scheduling and remote estimation problem studied in [16]. Given the sensory element, the authors have employed an encoder-decoder strategy coupled with a feedback Stackelberg solution to minimize the overall communication cost. To deal with the energy depletion and hardware malfunctioning in wireless sensor-based industrial network, authors in [17] proposed a software based framework, named as SD-WSN. The software-based SD-WSN architecture successfully addresses the problem of node failure, network reliability and management. Employment of FPGAs in the domain of industrial automation can be observed in [18]. Here, the embedded processor (Nios) inside the FPGA has been used to assimilate a Profinet software IP core. However, this work still belongs to the software-based implementation category as it did not involve FPGA fabrics for implementation.

First attempt to devise a hardware based Profinet implementation can be found in [19]. This FPGA-based implementation reveals the fact that the proposed architecture is re-configurable and thus, suitable for high-end industrial requirements. Recently, in [20], the authors have shown the FPGA-based implementation of Media Access Control (MAC) and Physical layer system (PHY) for industrial automation systems. From the above discussion, it can be established that FPGAs are evolving as a promising platform for industrial automation devices. However, an FPGA-based solution which provides a user friendly access to generate

specific network conditions is still in its infancy. Thus, in this paper, we have explored the FPGA-based NetJury device to devise a load generator for Profinet communication. The next section will elaborate our proposed *Profi-Load* framework.

III. PROFI-LOAD FRAMEWORK

In this section, we will describe how our proposed *Profi-Load* framework will function. In a nut-shell, the proposed framework generates packets (frames)¹ and place inter-frame gaps in such a way that the desired load can be maintained. Before describing the core theme of our strategy, we will now discuss the default parameters for generating the packet.

A. Parameters of a Packet

A network packet contains header fields which includes source and destination MAC addresses and is denoted as M . A packet also contains Ethertype (denoted as E) i.e., for Profinet packet, Ethertype would be “0x8892”, optional vLAN tag (denoted as v ; vlan id, vlan priority and vlan cfi) and payload size (denoted as Y) in bytes. MAC addresses M , Ethertype E and Payload Y are collectively represented as P in equation (1). For ease of visualization, a basic packet structure is shown in Figure 3. Apart from these main parameters, a packet also contains some other parameters including preamble: p , delimiter: d , frame check sequence: f and interframe gap: i . All these parameters are combined with the packet header fields to represent a single frame. We denote all such other fields as O overhead in equation (2). If the packet is v vLAN tagged then an extra 4-bytes get included in the overhead O .

Layer IP Packet						IP	IP Payload		
Layer 2 Frame			MAC destination	MAC source	VLAN tag	Ethertype or length	Payload	Frame check sequence	
Layer 1 Packet	Preamble	Start of delimiter							Interframe gap
	7 octets	1 octet	6 octets	6 octets	4 octets	2 octets	42-1500 octets	4 octets	12-? octets

Fig. 3. Structure of a packet.

$$P = \{M, E, Y\} \quad (1)$$

$$O = \{p, d, f, i, v\} \quad (2)$$

The overall size of a packet, S will include both O and P in bytes as shown in equation (3). Varying payload Y , P is of size 60, 128, 256, 512, 1020, 1514 bytes.

$$S = \{P, O\} \quad (3)$$

¹In this paper, we have used the terms *packets* and *frames* interchangeably

B. Load Generation Strategies

In this subsection, we will discuss the strategies adopted by the *Profi-Load* framework. As per the requirements of modern industrial automation devices, the load generator should be able to generate load with some specific constraints i.e., either by selecting the number of frames or the time duration.

CASE 1: Sending F Number of Frames for a Specified Load

Let us assume the case where *Profi-Load* has to send F number of frames with L % of load. Here, as the number of frames is fixed so in order to maintain the fixed (L %) load, *Profi-Load* will send frames with interframe gap (I). Hence, a certain gap between two consecutive frames should be maintained. This I can be calculated as:

$$I_L = S \times \left(\frac{1}{L} - 1\right) \quad (4)$$

$$I = I_d + I_L \quad (5)$$

In the equation (5), I_d denotes the minimum interframe gap of 12 bytes between two frames. However, to maintain the desired percentage of load, I_L is the additional interframe gap in bytes to be added while sending specific number of consecutive frames (say, 3) as shown in Figure 4. This scenario will be effective when the user is interested to employ *Profi-Load* for load generation by specifying a particular number of packets.

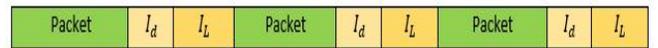


Fig. 4. Interframe gap between three consecutive frames to maintain desired load in the network.

CASE 2: Generating Load for T Time duration

Let us assume another scenario within which the user needs the *Profi-Load* to generate L % of load for the specified duration of T time units². Therefore, in T time period, *Profi-Load* will calculate and send F number of frames which will be passed through the Profinet network so that the required L % load can be achieved. The required F number of frames can be calculated as:

$$F = \frac{R}{S \times 8} \times L \times T \quad (6)$$

In equation (6), R denotes the available network bandwidth which can either be 100 Mbps (Fast Ethernet) or 1 Gbps (Gigabit Ethernet). However, as per our experimental observation, it has been observed that the elapsed time (T) i.e., the actual time consumed for sending F number of frames (obtained from equation 6) can be equal or slightly lower (of the order of microsec) than T . We will analyze this phenomenon in detail (refer, Section V, *Evaluation of*

²time units could be hours, minutes or even micro seconds as per the requirements

Case 2).

CASE 3: Generating Bursts of Specified Load

Given the user defined F number of frames and T time period, case 1 and 2 will result into a non-synchronized load generation. In real-time industrial environment, such load generation might not be efficient due to variable response timings of the IO devices set by the PLC controllers [2]. It might eventually cause the industrial devices to enter into the failure mode which is not desirable. To keep the devices under normal operation, *Profi-Load* should be able to generate load (packets) with specific delays. Therefore, the third scenario specifically works in this direction, group of packets (burst) are collectively send at some desired load for some specific time interval. After sending the required number of packets (burst), there will be some time-gap after which, the next burst will be transferred. The time duration in which the burst of packets are being transmitted is called “Burst interval” and the time-gap in between two bursts of packets is considered as a “Sleep Interval”.

IV. *Profi-Load* ON FPGA (NETJURY DEVICE)

A. Brief Description of NetJury

As the execution platform of our proposed *Profi-Load* framework, we have selected the NetJury device [7]. NetJury is based on Xilinx Zynq FPGA [21] integrated with a Dual-core ARM Cortex-A9 processor [22]. The FPGA fabric part is termed as PL (Programmable Logic) and an ARM component named as the Processing System (PS). PL is mainly responsible for hardware processing of real-time data and the PS is used to operate the PL logic unit via Linux based environment. NetJury supports a proprietary scripting language, named as NetJury Scripting Language (NSL) to program the PL component. The overall architecture of the device is shown in Figure 5.

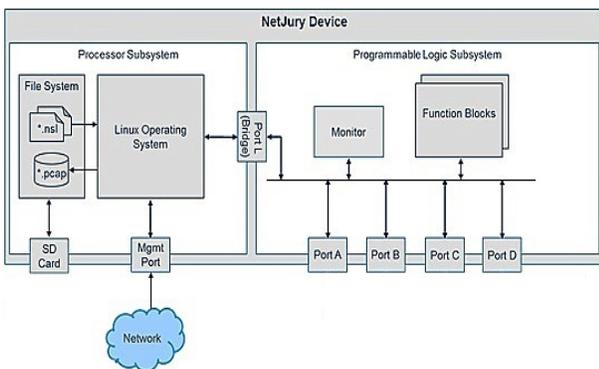


Fig. 5. Architectural view of the NetJury [7].

In Figure 5, the right block is the programming part (PL) and the left block (PS) provides the Linux environment on the ARM processor. The PL subsystem consists of four Ethernet ports operated at Gigabit Ethernet (1Gbps) and Fast Ethernet (100Mbps). Within the scripting mode, packet generation with manipulation up-to the application layer can

be carried out with all the Ethernet ports via NSL script, operated from the PS block. The Port L (eth1) is used to communicate between PS and PL. Monitor module helps the user to inspect and manipulate the traffic received on any of the FPGA ports. The user from the PS side can trigger the desired functionality in PL with NSL scripts generated and executed by high level programming language e.g. python. However, the user needs to turn on the scripting mode once at start for using the PL side of the NetJury.

B. *Profi-Load* inside NetJury: Human Machine Interface

NetJury by default is designed to send traffic while expecting a particular number of frames to be received on any of the desired port. Hence, to employ the *Profi-Load* framework on NetJury, the user needs to provide all the required information. In order to generate the load with certain specifications (as discussed in previous section via different cases) the user has to provide the following information (based on the requirements):

- Load %
- Source MAC address
- Destination MAC address
- Ethertype
- vLAN fields
- Packet size
- Load generating port of NetJury and its line rate
- Functionality
 - Burst feature
 - Time feature
 - Frame feature

Upon receiving the user input, NetJury will calculate F number of frames and I inter-frame gap (for each consecutive frames) for load generation. To access our *Profi-Load* framework efficiently, we have developed a web-based Human Machine Interface (HMI). This GUI interface will provide NetJury the required information for successful load generation shown in Figure 6. Once the user successfully logged in, the GUI will allow the user to provide the input arguments. However, the user can only make use of single feature at a time, i.e. it can either be Burst check, Time check or Frame check. Once the load is generated successfully, via HMI, NetJury will inform the user about the actual amount of feasible frames or time spent to generate required % of load.

V. EXPERIMENTAL VALIDATION

In this section, we detail the measurements that were carried out to test the efficacy of our *Profi-Load* framework for real-time traffic generation. Inside the NetJury, the synthetic packets are created by using pythonic modules such as Scapy and translated into NSL syntax for load generation. These packages are executed in Linux environment running on the PS side. As measurement tools, we have used Wireshark [23], Hilscher netANALYZER [24] and a tektronix DPO4054B Oscilloscope [25]. Moreover, accurate time measurements with Ethernet decoding are performed via MATLAB.

Fig. 6. Load generation interface for user input.

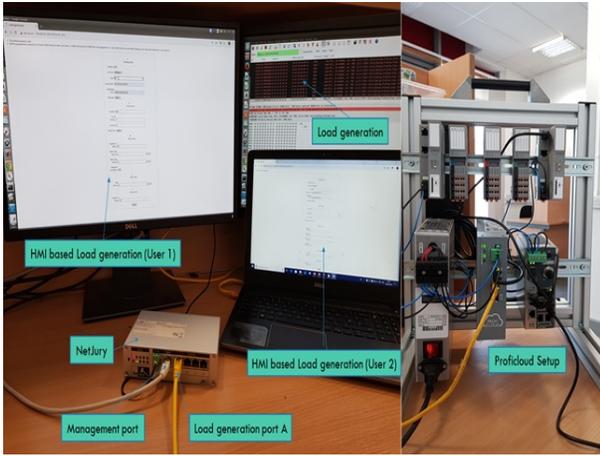


Fig. 7. Profi-Load setup.

Figure 7 illustrates the running setup of *Profi-Load* employing NetJury. The management port of the NetJury is connected with the Intra-net from where any connected device such as Laptop and other mobile devices (say, user 1 and 2) with user credentials can access the HMI interface for load generation. Wireshark measurement is carried out by connecting the packet generating port (port A as shown in Figure 7) of the NetJury to the Ethernet port of a PC running Wireshark application. Depending upon the OS level interrupt(s), the captured timestamps through Wireshark can vary from real-time measurements performed with Hilscher netANALYZER and tektronix DPO4054B oscilloscope. Now we will describe our experimental observations on the performances of *Profi-Load* over different cases.

1) *Evaluation of Case 1: (40 long frames at 25% load):* The frames are being sent with $P = 1514$ bytes. Alongside,

there will be an extra 7 bytes preamble, 1 byte start delimiter, 4 byte frame checksum and for Fast Ethernet, a default 12 bytes interframe gap. Therefore, according to equation (2), the overhead (O) is 24 bytes and the total size of the packet becomes $S = 1538$ bytes (refer equation 3).

To send 40 packets with 25% of load, according to equation (4), I_L becomes 4614 bytes and I_d is 12 bytes. Hence, interframe gap (I) that we should maintain becomes 4626 bytes (refer, equation 5).

After receiving all the input parameters via HMI interface, NetJury will calculate the F number of frames, I interframe gap and other parameters and will start operating. Figure 9 shows the Oscilloscope measurement of 40 long packets. It has been observed that the entire transmission of the 40 packets took 19.57 ms. We have also employed Wireshark to measure the transmission time and observed 19.17 ms for the transmission (please see Figure 11).

Matlab Based Analysis: Using Oscilloscope, we captured the *Profi-Load* generated packets with setup shown in Figure 8. P0 and P1 depict the probes / ports of the Oscilloscope / Hilscher netANALYZER coupled with a PCB board specifically made for connecting probes and ports. Each captured packet has been further evaluated via MATLAB. For this evaluation, we have introduced two new parameters: E_R which denotes packet lasting time in the Profinet network and E_L which denotes the specific time-gap after which each frame will be sent periodically to maintain the specified load L .

E_R can be found out for $R = 100$ Mbps link, as:

$$E_R = \frac{S \times 8}{R} \quad (7)$$

After finding the E_R , we can find the E_L as:

$$E_L = \frac{E_R}{L} \quad (8)$$

Theoretically, with the above two equations (equation 7 & equation 8), we can expect the values of $E_R = 123.04\mu s$ and $E_L = 492.16\mu s$ for $L = 25\%$ load.

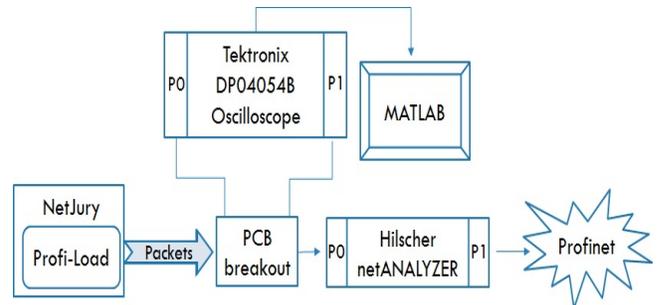


Fig. 8. Setup for capturing packets using tektronix DPO4054B Oscilloscope.

Experimentally, we have achieved the same results. In Figure 10, it can be observed that NetJury sent 40 packets with 25% load. A single packet lasts as expected for 123.04 μs and the NetJury sent each packet (upto 40 packets)

after a specific time gap (E_L) of 492.161 μs . From the above experimental evaluation, it is evident that NetJury can effectively generate load with proper timing efficiency.

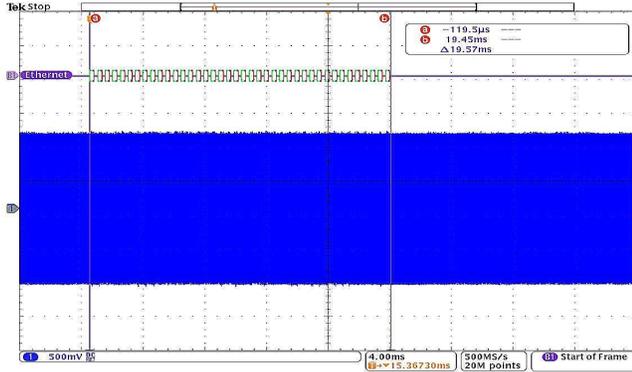


Fig. 9. Oscilloscope measurement of 40 long packets at 25% load.

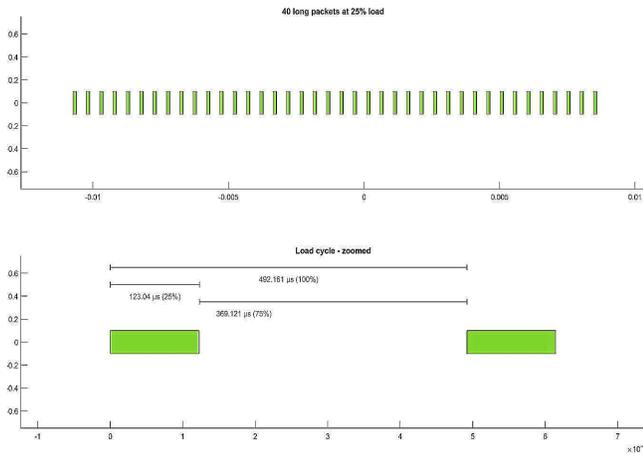


Fig. 10. MATLAB analysis of 40 long packets at 25% load.

2) *Evaluation of Case 1: (750 short packets at 25% load):* It has also been observed that *Profi-Load* is capable of performing efficiently in case of generating short frames with a specified load. In this case, the short frame size (S) is 84 bytes, where P is of 60 bytes and O contains 24 bytes.

In Figure 14, it can be seen that Wireshark detected that *Profi-Load* generates 750 short frames in 18.14 ms. A similar Matlab based analysis revealed the fact that NetJury generates 25% load by sending every frame with the interval of $E_L = 26.88\mu s$. Each packet lasts for $E_R = 6.72\mu s$ (refer, Figure 12). These measurements are also matched with the theoretically expected values of E_R and E_L .

3) *Evaluation of CASE 2: (Short packets at 25% load for 20 ms time period):* After receiving the specific inputs from the user, the *Profi-Load* framework will calculate the required number of frames that need to be sent for maintaining the load ($L\%$) for specific duration (T).

In this particular case, the size (S) of each frame is 84 bytes ($P = 60$ bytes and $O = 24$ bytes respectively). The speed of the network (R) is 100 Mbps. The required load (L) is 25% and the specified time duration (T) is 20ms. Hence,

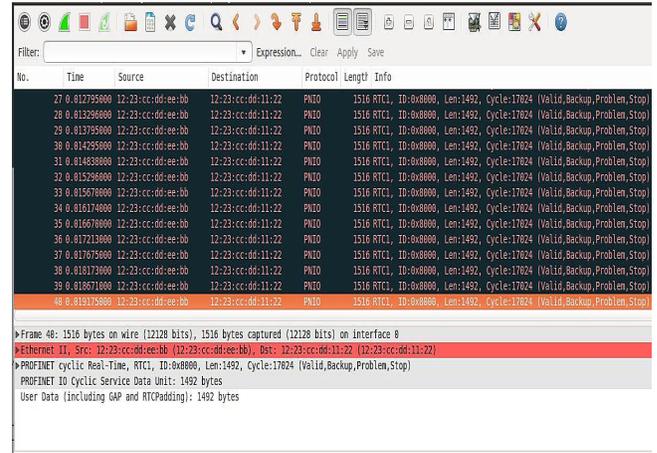


Fig. 11. Wireshark measurement of 25% load with 40 long packets.

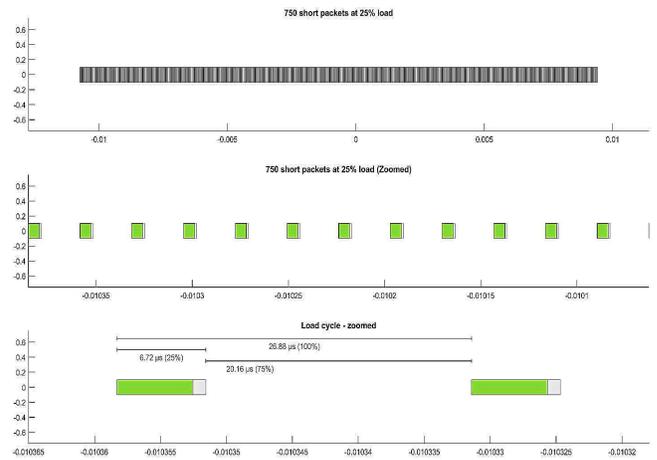


Fig. 12. MATLAB analysis of 750 short packets at 25% load.

as per equation (6), the number of required frames (F) which have to be sent, comes around to be 744.

These 744 frames/packets have to be sent with the specific interframe gap (I) which can be calculated from equation (5). Here, I can be found as

$$I = 12 + 252 = 264$$

As per our previous measurement strategies, Fig. 13 illustrates the Matlab based analysis. It has been observed that the NetJury can generate frames with correct / expected specifications. Each frame lasts for $E_R = 6.72\mu s$ and the $E_L = 26.88\mu s$ time gap, after which the next frame must be sent. Thus, by following this interval period (E_L), 744 packets / frames were sent for $T = 20ms$.

Critical Observation and Analysis:

We have previously mentioned in section III-B (Case 2) that the user specified duration (T) can slightly vary from the experimentally observed duration (T'). Here, we can calculate the time elapsed (T') for sending F number of frames as:

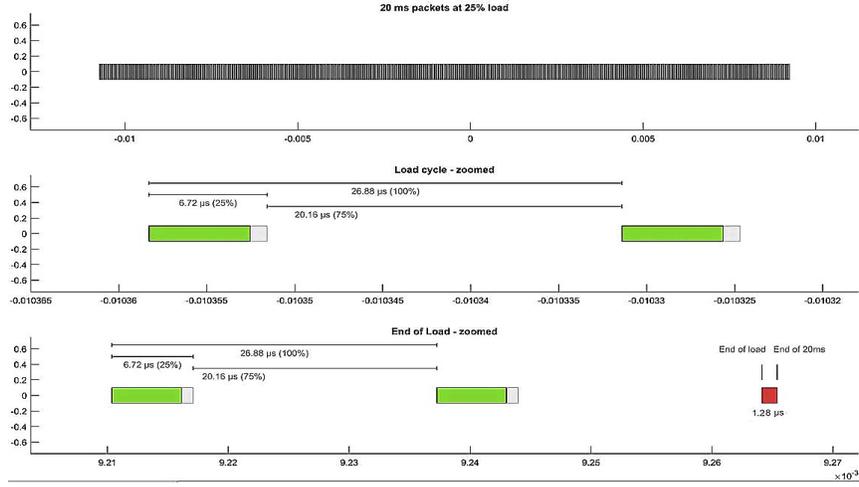


Fig. 13. MATLAB analysis of 25% load with 744 short packets in 20 ms.

No.	Time	Source	Destination	Protocol	Length	Info
737	8.818139008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
738	8.818139008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
739	8.818139008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
740	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
741	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
742	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
743	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
744	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
745	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
746	8.818148008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
747	8.818141008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
748	8.818141008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
749	8.818142008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)
750	8.818192008	12:23:cc:dd:ee:bb	12:23:cc:dd:ee:bb	PNIO	62	RTCL, ID:8x8808, Len: 38, Cycle:17824 (Valid,Backup,Problem,Stop)

Fig. 14. Wireshark measurement of 25% load with 750 short packets.

$$T' = E_L \times F \quad (9)$$

From (9), the actual elapsed time (T') for sending 744 frames becomes 19.99872ms. From Figure 13, it can be observed that this time difference (say, TD) as:

$$TD = 20 - 19.99872 = 1.28\mu s$$

This observation can be attributed to the fact that for the specific duration of 20 ms, *Profi-Load* found it feasible to send only 744 packets to maintain 25% load despite having a small TD . However, in order to compensate TD , if *Profi-Load* sends an extra frame then the elapsed time (T') would exceed the specified duration (T) as:

$$T' = 19.99872 + E_R > 20ms (T)$$

Hence, this observation shows how effectively the NetJury device operates for generating specified load with stringent time requirements.

No.	Time	Source	Destination	Protocol	Length	Info
5956	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5957	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5958	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5959	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5960	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5961	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5962	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5963	8.999645008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5964	2.000249008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5965	2.000249008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5966	2.001341008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5967	2.001341008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5968	2.001242008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
5969	2.001343008	10:10:10:10:10:10	26:63:36:06:09:1e	PNIO	1026	RTCL, ID:8x8808, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)

Fig. 15. Wireshark measurement of 25% load exhibiting the end of 1st Burst and start of 2nd Burst with 1s sleep time.

4) *Evaluation of CASE 3: (vLAN tagged 50% load for 20 bursts)*: In modern factory automation environment, different I/O devices demand different synchronizing/response times. Therefore, injecting a non-synchronous load (via case 1 and case 2) may force them to enter into the failure mode, thus, devices will stop functioning. Therefore, it will be effective to generate load with “burst feature”. This feature will enable user to specify number of bursts (each burst will contain a specific number of packets) to maintain the desired load. The time duration between two consecutive bursts is referred here as “Sleep Interval”.

Consider a scenario where the user would like to send 50% vLAN tagged Profinet load with 20 bursts. Both the “Burst Interval” and “Sleep Interval” are kept as 1 second. With $P = 1020$ bytes and having an extra 4-bytes v vLAN tag, the overhead becomes 28 bytes. Hence, the packet size (S) is of 1048 bytes.

After obtaining all these user specified parameters, *Profi-Load* will calculate the F number of frames to be sent using equation (6). It has been found that for maintaining 50% load

No.	Time	Source	Destination	Protocol	Length	Info
119240	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119241	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119242	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119243	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119244	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119245	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119246	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119247	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119248	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119249	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)
119250	39.06388000	10.10.10.10:10.10.10.10	26.63.36.06:89:1e	PKTD	1026	RTCL, ID:0x0800, Len: 998, Cycle:17824 (Valid,Backup,Problem,Stop)

Fig. 16. Wireshark measurement of 25% load exhibiting the total number of frames generated by 20 bursts.

there should be 5963 number of frames within a burst. Thus, 20 bursts will collectively generate around 119260 frames.

If we look into the Figure 15, we can identify that the 1st burst ends at 5963th frame. After the specified sleep interval of 1s, 2nd burst start sending frames. It can also be observed that the *Profi-Load* can transmit VLAN tagged packets as well with specified priority (7, in this case). Figure 16 establishes the fact that the *Profi-Load* successfully generates the required number of bursts with specified duration. It is evident that total 119260 frames were sent within 39 seconds i.e 20 seconds for transmission and 19 seconds for the sleep interval.

Currently, the burst feature keeps the burst interval or sleep interval same for all the user defined number of bursts. However, this feature can be extended for variable burst interval or sleep interval, i.e., 1s sleep interval between 1st and 2nd burst and 1ms sleep interval between 2nd and 3rd burst with burst intervals of 1.5s, 10ms and 30μs for 1st, 2nd and 3rd burst. Moreover, our proposed *Profi-Load* can also be employed to generate load other than the Profinet-based packets by changing the Ethertype along with a suitable payload size (please see Figure 6).

VI. CONCLUSION

The vast deployment of Profinet in modern industrial automation devices such as, Controllers and the associated IO Devices can lead to unknown and untested critical failures when network traffic flows unexpectedly higher than normal. In this paper, we have proposed an FPGA-based Profinet load generation framework, *Profi-Load*. This framework can generate desired load configurations such as, bits per second, the number and size of the packets and the foremost factor delay in between the packets or burst of packets to maintain the desired throughput. Comprehensive set of experiments on a Zynq-7000 FPGA based “NetJury” device, have showed that the proposed framework can effectively generate load under various user defined scenario. Moreover, the framework can easily be accessed via web based interface for successful load generation.

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