# Delayed Bit Interleaved Coded Sparse Code Multiple Access

Zeina Mheich, Zilong Liu, Pei Xiao and Amine Maaref

Abstract—Targeting to provide reliable short-packet communications with tens of bits in machine-type networks, we investigate a novel sparse code multiple access (SCMA) scheme called delayed bit-interleaved coded SCMA (DBIC-SCMA). At the transmitter side, a delay module is introduced between each channel encoder and SCMA mapper such that data bits from different channel codewords can be mapped to an identical SCMA codeword. We present the main components and principles for the transmitter of DBIC-SCMA, followed by a predecoding assisted receiver design which exploits systematic and rate-adaptive properties of certain channel codes. Our simulation results show that the proposed DBIC-SCMA leads to significant improvements in error rate performance over the classical BIC-SCMA scheme.

Index Terms-NOMA, SCMA, BICM, DBICM, LDPC, Turbo.

# I. INTRODUCTION

Bit-interleaved coded modulation (BICM) with Gray labeling can potentially approach the capacity of coded modulation (CM) systems with lower implementation complexity compared to its counterparts such as trellis-coded modulation or multi-level coded modulation. In BICM, a bit interleaver is inserted between the channel encoder and the modulation mapper to eliminate the correlation of coded bits mapped to the same modulation symbols. Recently, a variant of BICM called delayed BICM (DBICM) has been proposed [1]-[3]. DBICM outperforms the traditional BICM for certain constellations by taking advantage of the interblock memory [4] among multiple codewords [5]. This is achieved by introducing a delay module between the channel encoder and the modulation mapper such that data bits from different codewords can be mapped to an identical symbol. It is shown in [5] that DBICM can achieve higher capacity than BICM at the expense of a slight increase in latency. In particular, DBICM can achieve significant gains over BICM for non-standard constellations where Gray labeling may not be applicable<sup>1</sup>. Simulation results in [5] show

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<sup>1</sup>These constellations are widely used in optical communications as stated in [5].

that there is significant gain for half-16-QAM compared to marginal gain for standard 16-QAM.

Inspired by the above observation, we apply DBICM to SCMA whose constellations are mostly non-standard [6], [7]. SCMA is an emerging code-domain non-orthogonal multiple access (NOMA) paradigm which enables overloaded multiuser communications [8]. In NOMA schemes, the same resource, e.g. time/frequency/code, is used by multiple users, whereas conventional OMA schemes, e.g. time/frequency/code-division multiple access, rely on orthogonality to avoid interference between users [9]. As a result, NOMA has emerged as a promising technique to support efficient information exchange of massive communication devices in machine-type networks. Although the design of coded NOMA systems has been investigated in several works (e.g. [10], [11], [12]), DBIC-SCMA has not been reported in the literature, to the best of our knowledge. We consider short channel codes which are of particular interest to machine-type communications and ultra-reliable and low-latency communications (URLLC). A plus point of DBIC-SCMA is that it does not prevent other users from using BIC-SCMA in the same transmission. The main contributions and findings of this work are summarized as follows:

- We show that when an SCMA user employs DBIC-SCMA scheme, the gain is not limited to this specific user but also beneficial to the remaining ones which share the same resources. It is found that largest gain is attained when all the SCMA users employ DBIC-SCMA scheme.
- 2) We propose a novel receiver structure for DBIC-SCMA by exploiting systematic and rate-adaptive properties of certain low-density parity check (LDPC) codes in 5G New Radio (NR). The main idea is to perform LDPC pre-decoding when some partially received signals are received. Significant error rate performance gains are observed for the proposed receiver over the ones for DBIC-SCMA without pre-decoding and BIC-SCMA.

The rest of the paper is organised as follows. Section II-A introduces the transmitter structure and the proposed predecoding based receiver for DBIC-SCMA. Section III presents error rate performance using 5G compliant LDPC codes. Finally, Section IV concludes the paper. The key notations used in the paper are summarized in Table I.

Table I SUMMARY OF KEY NOTATIONS.

Notation	Meaning
J	Number of users
K	Number of resources
M	SCMA codebook size for each user
$u_j$	The <i>j</i> th SCMA user
$\mathbf{C}_{j}$	SCMA codebook of user $u_j$
$\vec{R}$	Channel code rate
$\mathbf{x}_{j}$	Transmitted SCMA codeword for user $u_j$
$\mathbf{h}_{j}$	The fading channel vector between user $u_j$
-	and the receiver
t	Time index
$\mathbf{c}_t^j$	The channel codeword of user $u_j$
n	The length of $\mathbf{c}_t^j$
$\pi_j$	Interleaver used by $u_j$
$\mathbf{c}_t^j(i)$	The <i>i</i> th sub-block of codeword $\mathbf{c}_t^j$
$T_i$	Delay attributed to the sub-block $\mathbf{c}_t^j(i)$
$m_{r_k \to u_i}^{(\tau)}(\mathbf{x})$	The belief message from resource $r_k$ to user $u_j$ at the
	$\tau$ th MPA iteration about the codeword <b>x</b> .
$m_{u_j \to r_k}^{(\tau)}(\mathbf{x})$	The belief message from user $u_j$ to resource $r_k$ at the
	$\tau$ th MPA iteration about the codeword <b>x</b> .
$\mathcal{I}_r(k)$	The set of user indices sharing resource node $r_k$
$\mathcal{I}_u(j)$	The set of resource indices used by user $u_j$

## II. DELAYED BIT-INTERLEAVED CODED SCMA

## A. SCMA system model

We consider an uplink SCMA system where J users share K resource nodes with K < J. In practice, SCMA is implemented using orthogonal frequency-division multiplexing (OFDM) where every resource node refers to a subcarrier channel. Each user is assigned with a multi-dimensional SCMA codebook consisting of M codewords. Every codeword is sparse and will be sent over K orthogonal resources. At the transmitter of user  $u_i$ , the SCMA mapper maps each vector of  $\log_2(M)$  coded bits to a multi-dimensional codeword  $\mathbf{x}_i$ selected from the corresponding SCMA codebook. The sparsity of the SCMA codewords implies a few number of users (less than J) colliding over the same resource. This enables the use of message passing algorithm (MPA) for multiuser detection performance close to maximum likelihood receiver. It is noted that every SCMA system can be represented by a factor graph with two sets of nodes: user nodes and resource nodes. A user node is connected to a resource node by an edge in the factor graph if and only if such a resource node is used by this specific SCMA user with active transmission.

#### B. DBIC-SCMA transmitter

A DBIC-SCMA transmitter structure is shown in the upper part of Fig. 1. For each user  $u_j$ , the channel coding module encodes a packet  $\mathbf{b}_t^j$  of k bits arriving at time slot t into a codeword of length n, where  $j \in \mathcal{J} = \{1, 2, \dots, J\}^2$ . The code rate is defined as R = k/n. After passing through the interleaver  $\pi_j$ , the interleaved codeword  $\mathbf{c}_t^j$  is split by the "serial to parallel" module into m sub-blocks

$$\mathbf{c}_{t}^{j}(i) = \left[c_{t,i\cdot n/m+0}^{j}, c_{t,i\cdot n/m+1}^{j}, \cdots, c_{t,i\cdot n/m+n/m-1}^{j}\right]^{T}, (1)$$

 $^{2}$ For simplicity of notation, we assume that the same channel code is applied to all SCMA users.

where  $m = \log_2(M)$ ,  $i \in \{0, 1, \dots, m-1\}$  and M denotes the size of the SCMA codebook for each user. A delay module  $Z^{-T_i}$  is then applied to each sub-block  $\mathbf{c}_t^j(i)$  which introduces a delay of  $T_i$ . Each delay unit is equal to a duration of n/m bits. The SCMA mapper<sup>3</sup> takes one input bit from each delayed sub-block and then maps the resulting m bits to an SCMA codeword  $\mathbf{x}_j$  occupying K resource nodes. The lower part of Fig. 1 presents an example of two delayed bit streams involving four codewords, where  $T_0 = 0$  and  $T_1 = 1$ . For ease of presentation, the following notations are used in the lower part of Fig. 1:

$$\begin{array}{ll} A_0 \triangleq \mathbf{c}_t^j(0), & A_1 \triangleq \mathbf{c}_t^j(1), \\ B_0 \triangleq \mathbf{c}_{t+1}^j(0), & B_1 \triangleq \mathbf{c}_{t+1}^j(1), \\ C_0 \triangleq \mathbf{c}_{t+2}^j(0), & C_1 \triangleq \mathbf{c}_{t+2}^j(1), \\ D_0 \triangleq \mathbf{c}_{t+3}^j(0). \end{array}$$

Moreover, in the sequel, the concatenation of  $A_0$  and  $A_1$  will be called codeword A. Similarly, we have codeword B. The lower part of Fig. 1 shows that each SCMA codeword is associated with a vector of two bits  $(d_0, d_1)$  which are from two different codewords, i.e., the bit  $d_0$  carries a coded bit from the current codeword while  $d_1$  carries a coded bit from the previous codeword. In this work, we use LDPC codes in 5G NR as the channel code.



Figure 1. A DBIC-SCMA transmitter with an illustration of m = 2 delayed bit streams for SCMA encoding (M = 4, t = 0). Natural block interleaving is introduced as the SCMA mapper takes m delayed bit streams slice by slice.

## C. DBIC-SCMA receiver design

The received signal y can be written as

$$\mathbf{y} = \sum_{j=1}^{J} \operatorname{diag}(\mathbf{h}_j) \mathbf{x}_j + \mathbf{n},$$
(2)

where  $\mathbf{h}_j = (h_1^j, \cdots, h_K^j)^T$ , where  $h_k^j \sim \mathcal{CN}(0, 1)$ , denotes the channel vector between user  $u_j$  and the receiver, and  $\mathbf{n} = (n_1, \cdots, n_K)^T$  with  $n_k \sim \mathcal{CN}(0, N_0)$  denotes the complex additive white Gaussian noise vector. By exploiting the sparsity of SCMA codebooks, efficient MPA can be applied which leads to error rate performance approaching to that of the maximum likelihood detector. The complexity of MPA is  $\mathcal{O}(M^{d_f})$  where  $d_f$  is the number of users sharing an identical resource node. MPA iteratively computes an approximation of the maximum *a posteriori* value by exchanging belief messages between users and resource nodes in the Tanner graph associated to the SCMA system [8]. Let  $m_{u_j \to r_k}^{(\tau)}(\mathbf{x})$  denote the belief message associated with the SCMA codeword  $\mathbf{x}$  which is transmitted from user node  $u_j$ , to resource node  $r_k$  at the

 $<sup>^{3}</sup>$ i.e., SCMA encoder which is equivalent to the modulation mapper in DBICM.

 $\tau$ -th iteration of the MPA. Similarly, denote by  $m_{r_k \to u_j}^{(\tau)}(\mathbf{x})$  the belief message from resource node  $r_k$  to user node  $u_j$ .  $m_{r_k \to u_j}^{(\tau)}(\mathbf{x})$  and  $m_{u_j \to r_k}^{(\tau)}(\mathbf{x})$  are given by [13]:

$$m_{r_k \to u_j}^{(\tau)}(\mathbf{x}) = \sum_{\substack{\mathbf{x}_j = \mathbf{x} \\ i \in \mathcal{I}_r(k) \setminus \{j\} \\ \mathbf{x}_i \in \mathbf{C}_i}} \frac{1}{\pi N_0} e^{-\frac{|y_k - \sum_{i \in \mathcal{I}_r(k)} h_k^i x_{ki}|^2}{N_0}} \\ \cdot \prod_{i \in \mathcal{I}_r(k) \setminus \{j\}} m_{u_i \to r_k}^{(\tau-1)}(\mathbf{x}_i),$$
(3)

and

$$m_{u_j \to r_k}^{(\tau)}(\mathbf{x}) = \alpha_j \cdot \prod_{\ell \in \mathcal{I}_u(j) \setminus \{k\}} m_{r_\ell \to u_j}^{(\tau)}(\mathbf{x}), \qquad (4)$$

respectively, where  $C_i$  denotes the SCMA codebook of user  $u_i$ ,  $\alpha_j$  is a normalization factor,  $\mathcal{I}_r(k)$  and  $\mathcal{I}_u(j)$  are the set of user indices sharing resource node  $r_k$  and the set of resource indices used by user  $u_j$ , respectively. At the initial iteration  $\tau = 0$ , we assume that

$$m_{u_j \to r_k}^{(0)}(\mathbf{x}) = 1/M, \quad \forall j \in \mathcal{J}, k \in \mathcal{I}_u(j).$$
(5)

MPA terminates, when a predefined maximum number of iterations is reached or when the convergence of the messages is achieved. The receiver of BIC-SCMA is composed of the SCMA detector (MPA) followed by the de-interleavers and the channel decoders. A turbo-like receiver may be implemented to enhance the error rate performance of BIC-SCMA by passing *a priori* messages from channel decoders to the SCMA detector [12]. In the following sub-section, we propose a novel DBIC-SCMA receiver which explores some excellent structures of certain channel codes.

1) Proposed Pre-decoding Assisted Receiver: The proposed receiver for DBIC-SCMA carries out pre-decoding based on partially received signals. Hence, it requires systematic and rate-adaptive channel code with  $R \leq 1/2$ , so that a codeword c can be split into two parts:  $c = [c_s, c_p]$ , where  $c_s$  and  $c_p$ are the parts carrying the information bits and parity bits, respectively. By noting the fact that natural block interleaving is introduced as the SCMA mapper takes m delayed bit streams slice by slice, the interleaver  $\pi_j$  in Fig. 1 may be omitted. Over the time window of each sub-block group<sup>4</sup>, the received signals carry information related to delayed and non-delayed messages from multiple codewords. After multiuser detection using MPA, the receiver can decode both non-delayed and delayed messages utilizing the fact that the channel code is systematic and rate-adaptive. Assuming that all sub-block groups have the same length,  $n_g = n/m$  c.u., the receiver can decode the non-delayed message with a rate equal to  $k/n_q$ . For the delayed messages, the receiver can decode them using current and past observations. When the decoding of one message (or more) is successful, MPA can be re-run to take advantage of the successfully decoded bit to improve the logarithm likelihood ratios (LLR) of the remaining bits.

Let us revisit the example shown in the lower part of Fig. 1. If the codeword A is decoded correctly in the first sub-block group (i.e.,  $A_0$  occupying channel uses from 1 to

n/2), then the receiver can obtain the remaining bits of the codeword transmitted in the second sub-block group (i.e.,  $A_1$  occupying channel uses from n/2 + 1 to n) by re-encoding the estimated packet  $\hat{\mathbf{b}}_t$ . Upon obtaining the whole codeword A, the receiver may remove the interference to  $B_0$  caused by  $A_1$ . More specifically, when the first bit  $d_0$  of vector  $(d_0, d_1)$  is known, one may use "MPA with partially known messages (MPA-PNM)", i.e., the effective codebook size can be reduced to 2 from 4. On the other hand, if  $A_0$  is not decoded correctly, the channel decoder can feedback LLRs of the bits associated with  $A_1$  as extrinsic information to be used by MPA in the next decoding iteration [1].

In order to implement MPA-PNM, the DBIC-SCMA receiver requires a feedback from the channel decoder to the MPA. The feedback carries *a priori* LLRs of the bits  $\mathbf{d}_j = (d_0^j, d_1^j, \dots, d_{m-1}^j)$  to decode the SCMA codeword of user  $u_j$ . Assume that  $\tilde{m}_j$  bits of  $\mathbf{d}_j$  are known because they belong to codewords which are decoded correctly by the receiver<sup>5</sup>. Let  $\mathcal{D}_j$  denotes the set of these  $\tilde{m}_j$  bits. As a result, the receiver can determine the effective codebook  $\tilde{\mathbf{C}}_i$  for user  $u_i$  which consists of all codewords in  $\mathbf{C}_i$  with bit vectors satisfying  $\mathcal{D}_j$ . Thus, the effective codebook size reduces to  $\tilde{M}_j = 2^{m-\tilde{m}_j}$ . Then, the message update in (3) will use  $\tilde{\mathbf{C}}_i$  instead of  $\mathbf{C}_i$ which reduces the complexity of MPA:

$$m_{r_k \to u_j}^{(\tau)}(\mathbf{x}) = \sum_{\substack{\mathbf{x}_j = \mathbf{x} \\ i \in \mathcal{I}_r(k) \setminus \{j\} \\ \mathbf{x}_i \in \tilde{\mathbf{C}}_i}} \frac{1}{\pi N_0} e^{-\frac{|y_k - \sum_{i \in \mathcal{I}_r(k)} h_k^i x_{ki}|^2}{N_0}} \\ \cdot \prod_{i \in \mathcal{I}_r(k) \setminus \{j\}} m_{u_i \to r_k}^{(\tau-1)}(\mathbf{x}_i),$$
(6)

and

$$m_{u_j \to r_k}^{(0)}(\mathbf{x}) = 1/\tilde{M}_j, \quad \forall j \in \mathcal{J}, k \in \mathcal{I}_u(j), \mathbf{x} \in \tilde{\mathbf{C}}_j.$$
(7)

Fig. 2 shows the decoding flow at the proposed pre-decoding assisted receiver. It is seen that there are two decision making modules. For the upper decision making module (marked in yellow), "Correct decoding" is decided based on the decoding results from all the J users: "yes" implies that at least one user has decoded successfully, whereas "no" means no user has passed the CRC check.

Due to the iterative nature of the MPA, the reduced effective codebook size for one user may be interpreted as a reduction of the interference to other users. In this way, users can help each other to improve their error rate performance. As such, it is expected that DBIC-SCMA is capable of offering substantial gain over BIC-SCMA. To further improve the performance of DBIC-SCMA, a turbo-like combination of iterative detection and iterative decoding can be implemented similarly to the turbo-like receiver of the BIC-SCMA scheme [12].

2) Classical DBIC-SCMA Receiver Without Pre-decoding: A second class of DBIC-SCMA receiver, called "classical receiver", does not rely on pre-decoding and hence does not require the channel code to be systematic and rate-adaptive. The main difference with the proposed pre-decoding assisted

<sup>&</sup>lt;sup>4</sup>In Figure 1, a sub-block group, e.g.,  $A_0$  or  $A_1$ , occupies n/2 channel uses (c.u.)

<sup>&</sup>lt;sup>5</sup>This can be done by the introduction of CRC or by using channel codes with high error detection probability.



Figure 2. Decoding flow of the proposed pre-decoding assisted receiver.

receiver is that the classical receiver needs to receive all the sub-block groups related to a complete channel codeword before carrying out the decoding.

## **III. SIMULATION RESULTS**

We consider an SCMA system of 6 users sharing 4 resources. We use the SCMA codebooks of size M = 4 [14]<sup>6</sup> and the LDPC codes proposed for 5G-NR in 3GPP standard<sup>7</sup>. For the DBIC-SCMA system, we set  $T_0 = 0$  and  $T_1 = 1$ . The parameters of the LDPC codes used in our simulations are the following: 1) R = 1/5, frame length = 100 bits; 2) R = 1/3, frame length = 132 bits; 3) R = 2/5, frame length = 50 bits. Since these 5G-NR LDPC codes are irregular, the natural block interleaving of DBIC-SCMA system allows us to take advantage of the bit-reliability (BR) mapping [15], [16] to facilitate the decoding.

Fig. 3 shows the average bit error rate (BER) and frame error rate (FER) performances in uplink Rayleigh fading channels for both BIC-SCMA and DBIC-SCMA. It can be observed that DBIC-SCMA achieves significant gains (e.g., at R = 2/5, about 2.5 dB gain in terms of BER and FER) comparing to BIC-SCMA. Moreover, the curves show that the proposed pre-decoding assisted DBIC-SCMA receiver outperforms the classical DBIC-SCMA receiver. Fig. 4 shows the effect of Turbo iterations between the SCMA detector and the LDPC decoder on the error rate performance. The performances of both BIC-SCMA and DBIC-SCMA schemes are enhanced by employing a turbo-like receiver.

Fig. 5 shows the BER performance of all users in uplink Rayleigh fading channels, in which BIC-SCMA and DBIC-SCMA (with the proposed pre-decoding assisted receiver) are adopted by N users and the remaining 6 - N users, respectively, where  $N \in \{0, 3, 6\}$ . Note that N = 0 corresponds to the case when all users employ DBIC-SCMA and N = 6corresponds to the case when all users employ BIC-SCMA. It can be observed that even when DBIC-SCMA is applied to a part of users, the BER performances of all users get improved. This is because the enhanced LLRs related to the coded bits of a certain user also help the remaining users sharing the same resources in the MPA decoding.

## **IV. CONCLUSIONS**

In this paper, we have proposed a novel DBIC-SCMA scheme with the principle of its transmitter and receiver, where

<sup>6</sup>Other codebooks could be used as well, e.g. [7].

the latter is based on pre-decoding of partially received signals by exploring certain LDPC codes with systematic and rateadaptive properties. We have shown that DBIC-SCMA offers substantial gains over the conventional BIC-SCMA scheme using short length LDPC codes for 5G-NR. The noticeable gains come from the fact that an improvement in the error rate performance of one DBIC-SCMA user helps boost the performance of the remaining ones.

It is noted that we have used MPA for SCMA detection. The complexity of the MPA detector grows exponentially with the maximum number of users sharing the same resource. As a future work, one may investigate the performance of DBIC-SCMA scheme using a low-complexity SCMA detector based on, for example, expectation propagation algorithm [17].

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<sup>&</sup>lt;sup>7</sup>http://www.3gpp.org/ftp//Specs/archive/38\_series/38.212/



Figure 3. Average error rate performance in uplink.



Figure 4. Uplink average error rate performance. R = 1/5. IT denotes the number of turbo-iterations.

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Figure 5. Uplink BER performance. R = 1/5. N refers to the number of users using BIC-SCMA while the remaining users are using DBIC-SCMA (Proposed).